HelixIPLM

PERFORCE

Save Time and Boost Chip Design Productivity with Helix IPLM Cache

High-speed, hierarchical workspace management helps remote design teams work faster and more efficiently — so you can meet your go-to-market goals.

IPLM Cache is an add-on feature available in Helix IPLM (Formerly Methodics IPLM) that allows globally distributed chip design teams to access the files they need at lightning speed. Instead of having to wait 20… 30… 40 minutes (or hours!) to pull down files, designers can get to work right away, saving valuable time and boosting productivity.

IPLM Cache cuts down workspace population time by building and updating workspaces from complex, versioned IP hierarchies. And it minimizes disk space through more efficient IP resource file management.

How much time and disk space savings can you expect? For example, with 20 user workspaces at 10G each:

- Workspace creation time is close to zero (after initial workspace creation).
- Total disk space consumed is 10G.

Enhance Workspace Performance

With IPLM Cache, design workspace setup is optimized to enhance workspace performance:

- Fully redundant, high availability architecture ensures designers can always access the files they need quickly and efficiently.

- Self-managed cache with automatic workspace population and cleanup.
- Rapid workspace creation with softlinks to IPs in the cache.
- Ability to pre-populate the cache with new IP versions.
- Flexible architecture to support multiple distributed teams and projects.

Help your remote design teams work faster and more efficiently with **Helix IPLM Cache**. Contact us today to learn more.